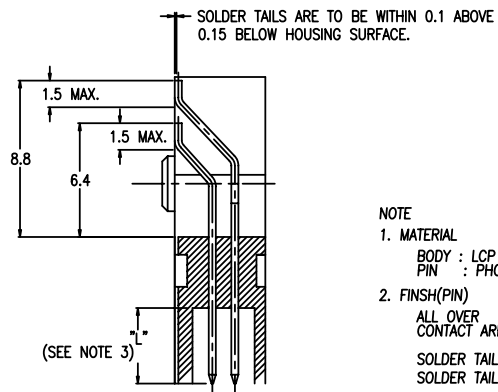
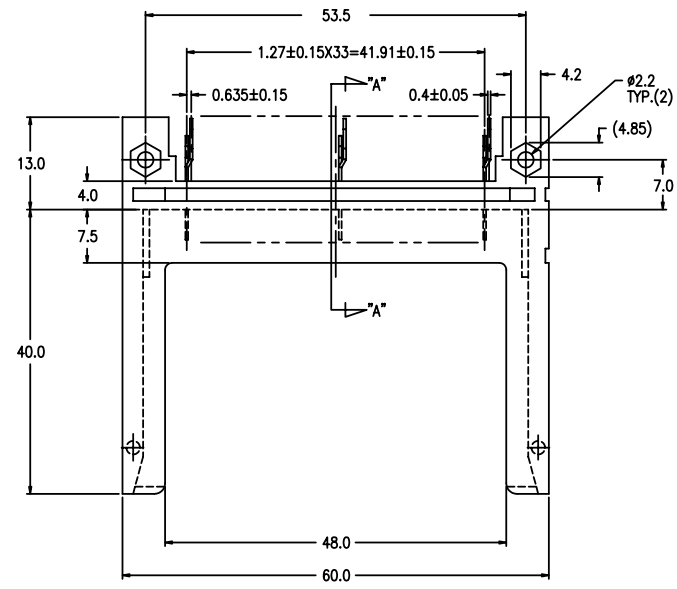
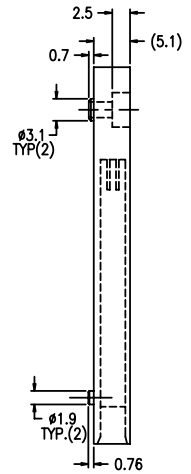


PRODUCT No.  
90937-001  
90937-001LF



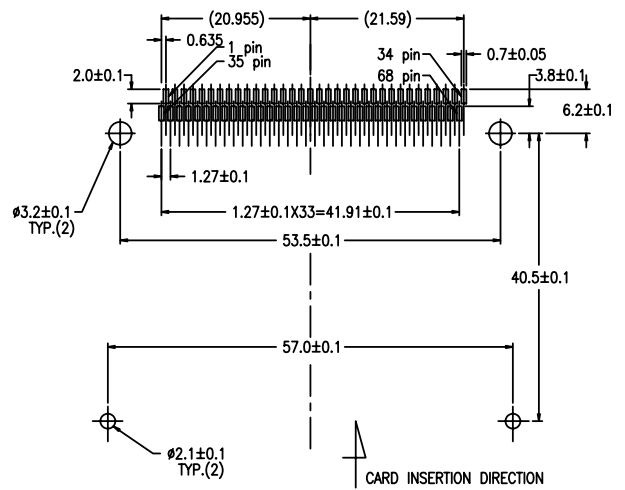
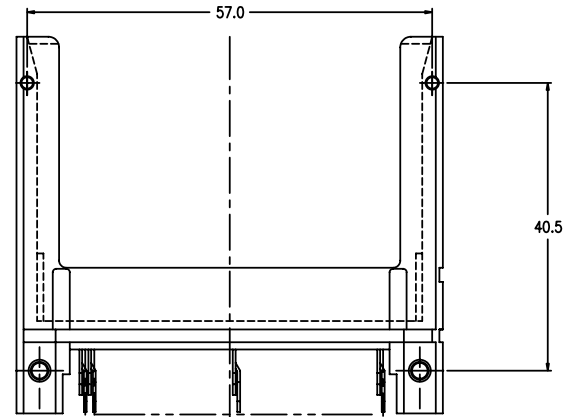
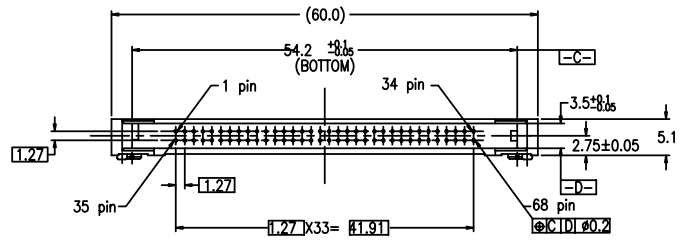
REVISIONS				
REV	DESCRIPTION	ECN	BY	DATE
3	REVISED		HA	6/11/92
4	REVISED		KS	11/18/93
A	RELEASED	J30167	KS	6/1/93
B	REVISED(Add "LF")	HOE-0031	MZ	03/08/06



SECTION "A-A" (5/1)

- NOTE
- MATERIAL  
BODY : LCP UL94V-0 BLACK  
PIN : PHOSPHOR BRONZE
  - FINSH(PIN)  
ALL OVER : Ni UNDER PLATING 0.5um THK MIN.  
CONTACT AREA : Au 0.1um THK MIN.  
OVER Pd/Ni 0.5um THK MIN.  
SOLDER TAIL(Tin Lead option) : Sn/Pb 2.5um THK MIN.  
SOLDER TAIL(Lead free option) : Pure Tin 2.5um THK MIN.
  - DIMENSION "L"  

1,17,34,35,51,68PIN	5.0±0.1
36,67PIN	3.5±0.1
OTHERS	4.25±0.1
  - CARD SLOT AND PIN DIMENSIONS PER JEIDA/PCMCIA STANDARDS
  - IF LEAD FREE P/N.THE HOUSING WILL WITHSTAND EXPOSURE TO 260°C PEAK TEMPERATURE FOR 10 SECONDS IN A WAVE SOLDER APPLICATION WITH A 1.00MM MINIMUM THICK CIRCUIT BOARD.
  - IF LEAD FREE P/N.THE PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DISCRIBLED IN GS-22-008
  - LEAD FREE P/N PACKAGING MEETS GS-14-920 SPECIFICATION

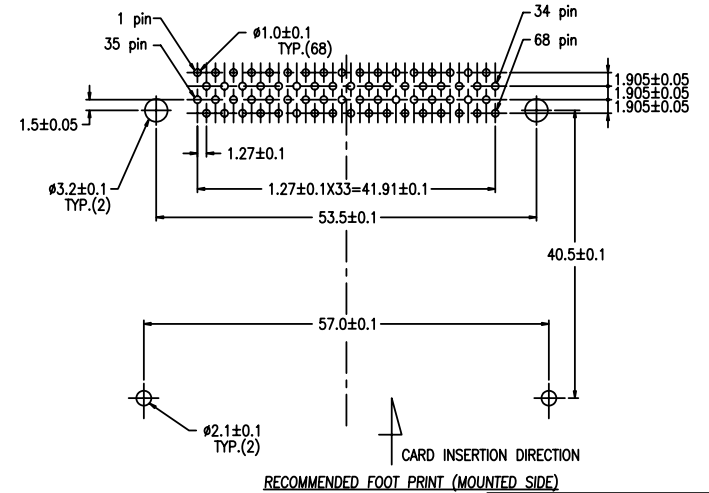
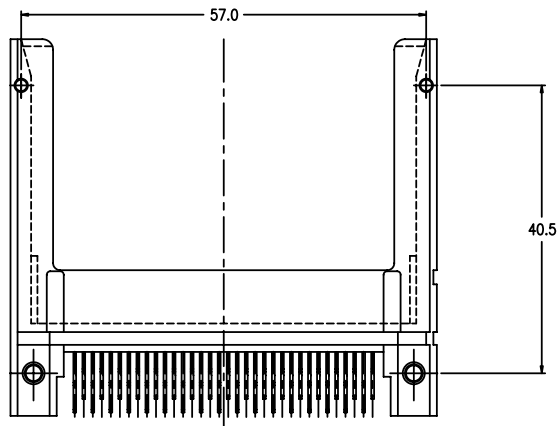
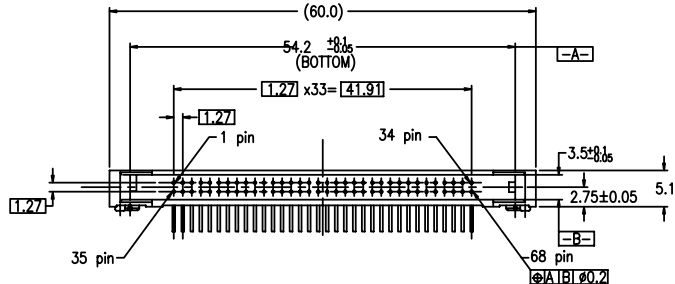
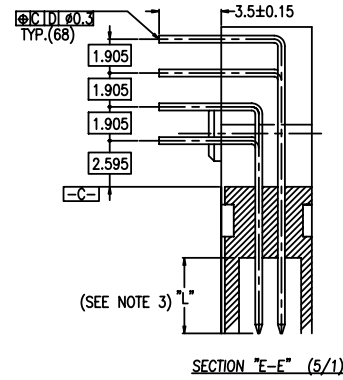
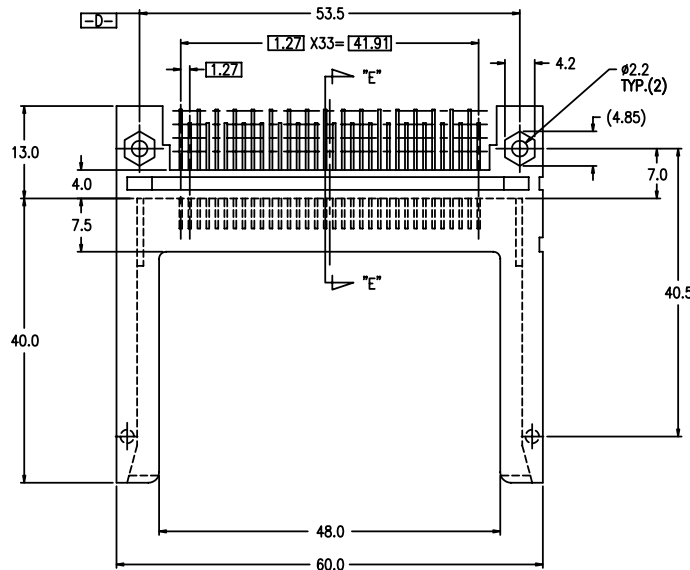
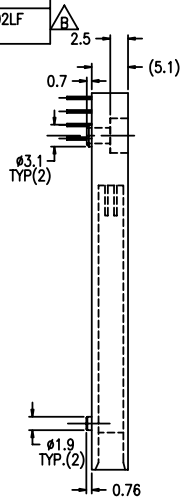


RECOMMENDED FOOT PRINT (MOUNTED SIDE)

SHEET INDEX	ISSUE	B	B	B	B					BERG ELECTRONICS			
SH NO.	1	2	3	4					TITLE				
TOLERANCES UNLESS OTHERWISE SPECIFIED	±0.3				MAT	DR	DATE	CUSTOMER COPY	68POS. HEADER ASS'Y				
LINEAR	SEE NOTE 1				ENGR	S.Dhormi	6/1/93	COPY	JEIDA / PCMCIA TYPE I, II				
ANGLES	SEE NOTE 2				FINISH	CHK		THIRD ANGLE PROJ	SCALE	SIZE	DWG. NO.	REV	SHEET
					APPD			UNIT	2/1	C	90937	B	1 of 4

PRODUCT No.  
90937-002  
90937-002LF

REVISIONS				
REV	DESCRIPTION	ECN	BY	DATE
5	REVISED		HA	6/11/92
6	REVISED		KS	11/18/92
A	RELEASED	J30167	KS	6/1/93
B	REVISED(Add "LF")	h06-0031	MZ	03/08/06



NOTE

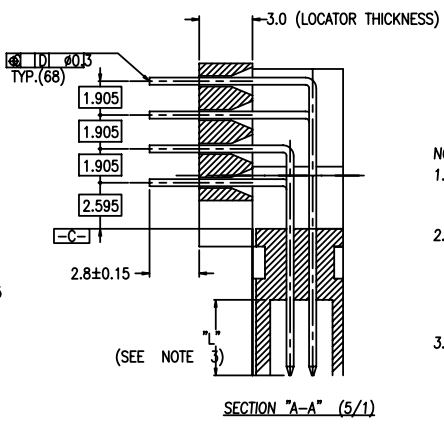
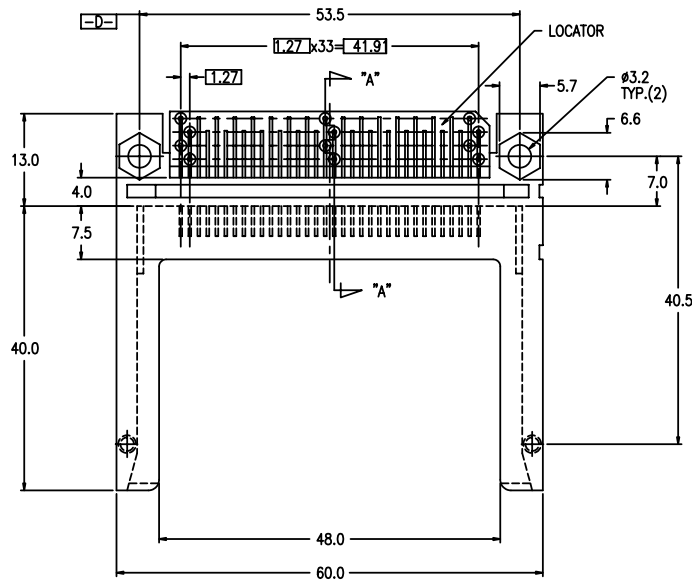
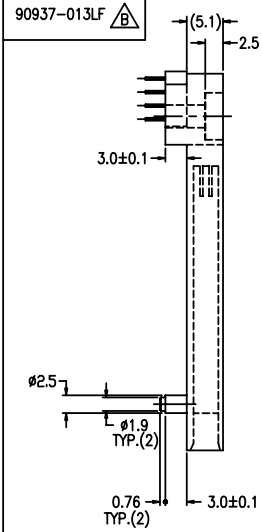
- MATERIAL  
BODY : LCP UL94V-0 BLACK  
PIN : PHOSPHOR BRONZE
- FINISH(PIN)  
ALL OVER : Ni UNDER PLATING 0.5um THK MIN.  
CONTACT AREA : Au 0.1um THK MIN.  
OVER Pd/Ni 0.5um THK MIN.  
SOLDER TAIL(Tin Lead option) : Sn/Pb 2.5um THK MIN.  
SOLDER TAIL(Lead free option) : Pure Tin 2.5um THK MIN.
- DIMENSION "L"  

1,17,34,35,51,68PIN	5.0±0.1
36,67PIN	3.5±0.1
OTHERS	4.25±0.1
- CARD SLOT AND PIN DIMENSIONS PER JEIDA/PCMCIA STANDARDS
- IF LEAD FREE P/N.THE HOUSING WILL WITHSTAND EXPOSURE TO 260°C PEAK TEMPERATURE FOR 10 SECONDS IN A WAVE SOLDER APPLICATION WITH A 1.00MM MINIMUM THICK CIRCUIT BOARD.
- IF LEAD FREE P/N.THE PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DISCRIBLED IN GS-22-008
- LEAD FREE P/N PACKAGING MEETS GS-14-920 SPECIFICATION

SHEET INDEX	ISSUE										
	SH NO.										
TOLERANCES UNLESS OTHERWISE SPECIFIED	±0.3	MAT	SEE NOTE 1	DR	S.Ihmani	DATE	6/1/93	CUSTOMER COPY	TITLE	68POS. HEADER ASS'Y	
LINEAR		FINISH		ENGR				THRU ANGLE PRO	JEIDA / PCMCIA TYPE I, II	SCALE	2/1
ANGLES		SEE NOTE 2		CHK				UNIT	MM	SIZE	C
				APPD				REV SHEET	90937	DWG. NO.	B
											2 of 2

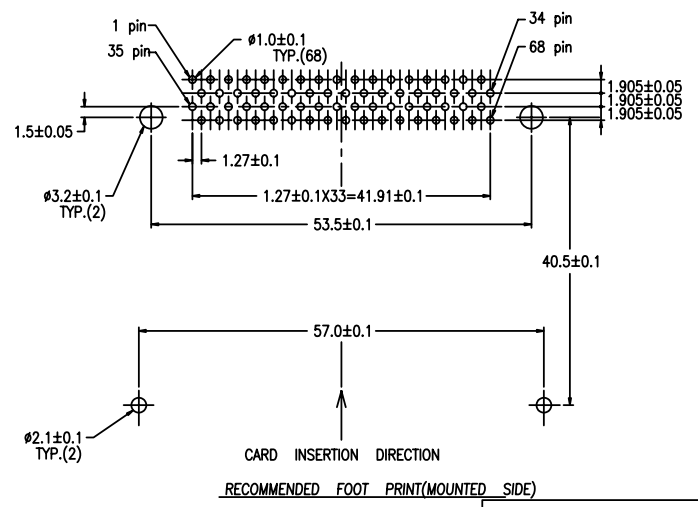
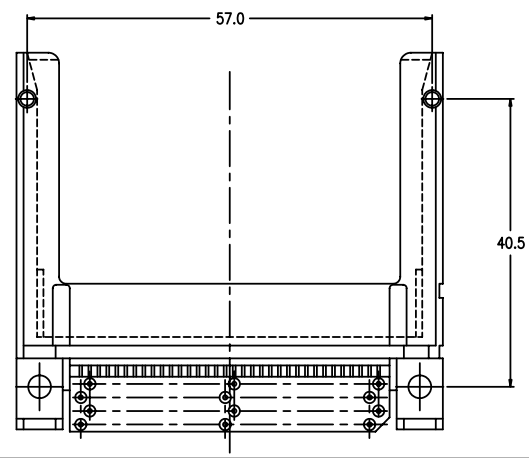
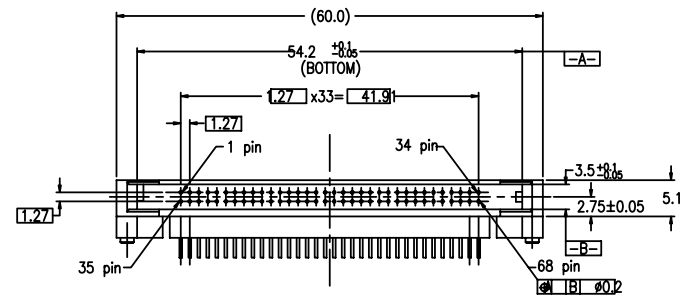
PRODUCT No.  
90937-013  
90937-013LF

REVISIONS				
REV	DESCRIPTION	ECN	BY	DATE
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2	REVISED		KS	10/14/92
A	RELEASED	J30167	KS	6/1/93
B	REVISED(Add "LF")	no6-0031	MZ	03/08/06



- NOTE
- MATERIAL  
BODY : LCP UL94V-0 BLACK  
PIN : PHOSPHOR BRONZE
  - FINISH(PIN)  
ALL OVER : Ni UNDER PLATING 0.5um THK MIN.  
CONTACT AREA : Au 0.1um THK MIN.  
OVER Pd/Ni 0.5um THK MIN.  
SOLDER TAIL(Tin Lead option) : Sn/Pb 2.5um THK MIN.  
SOLDER TAIL(Lead free option) : Pure Tin 2.5um THK MIN.
  - DIMENSION "L"  

1,17,34,35,51,68PIN	5.0±0.1
36,67PIN	3.5±0.1
OTHERS	4.25±0.1
  - CARD SLOT AND PIN DIMENSIONS PER JEIDA/PCMCIA STANDARDS
  - IF LEAD FREE P/N.THE HOUSING WILL WITHSTAND EXPOSURE TO 260°C PEAK TEMPERATURE FOR 10 SECONDS IN A WAVE SOLDER APPLICATION WITH A 1.00MM MINIMUM THICK CIRCUIT BOARD.
  - IF LEAD FREE P/N.THE PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DISCRIBLED IN GS-22-008
  - LEAD FREE P/N PACKAGING MEETS GS-14-920 SPECIFICATION



SHEET INDEX	ISSUE SH NO.	MAT	DR S. Dharmori	DATE 6/1/93	CUSTOMER COPY	
TOLERANCES UNLESS OTHERWISE SPECIFIED	±0.3	SEE NOTE 1	ENGR		TITLE 68POS. HEADER ASSY	
LINEAR		SEE NOTE 2	CHK		JEIDA / PCMCIA TYPE II	
ANGLES			APPD		SCALE 2/1	

